

FIG. 1A

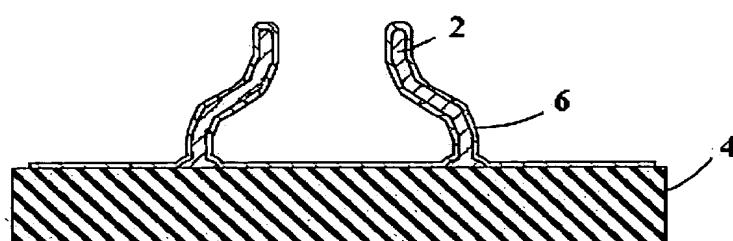


FIG. 1B

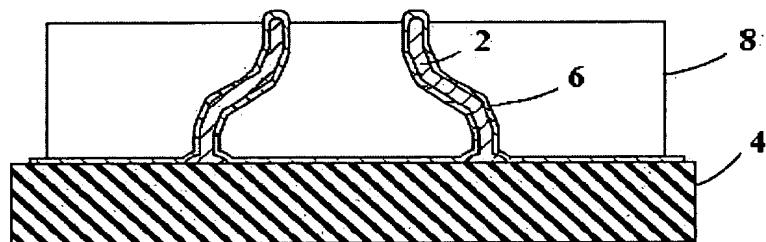


FIG. 1C

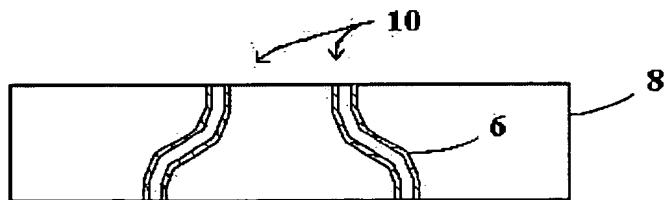


FIG. 1D

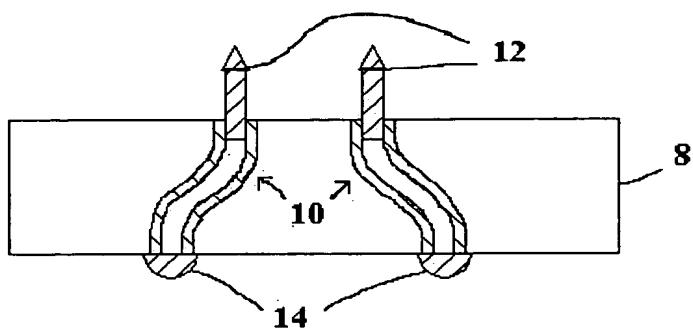


FIG. 2

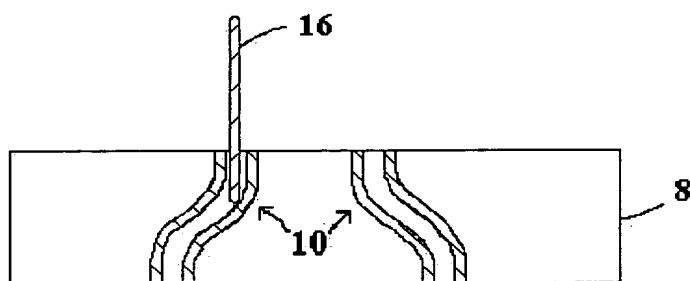


FIG. 3A

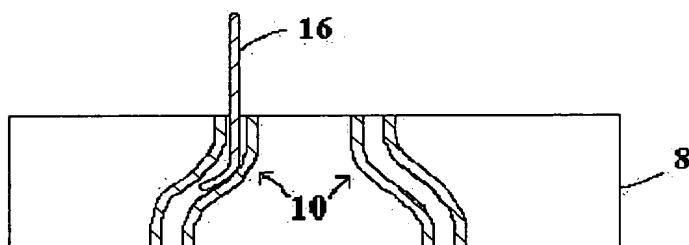


FIG. 3B

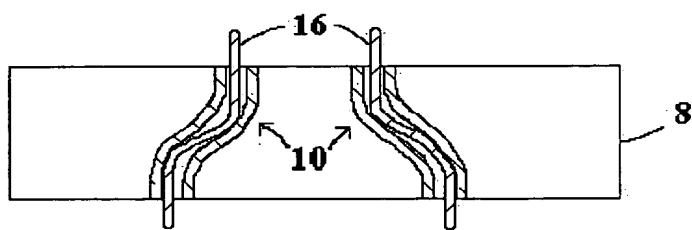


FIG. 3C

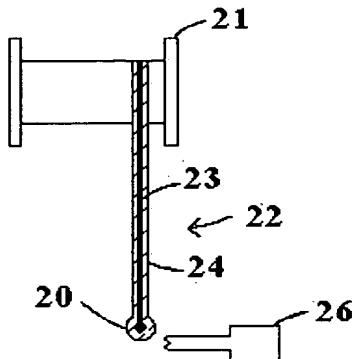


FIG. 4A

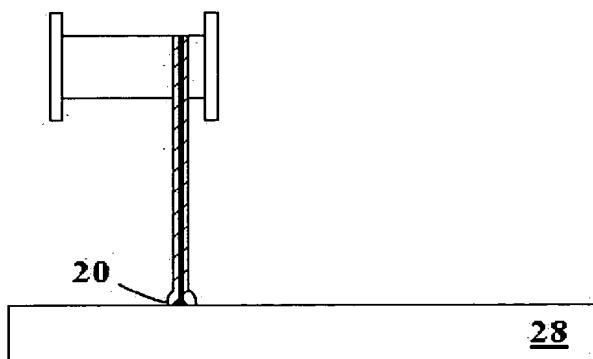


FIG. 4B

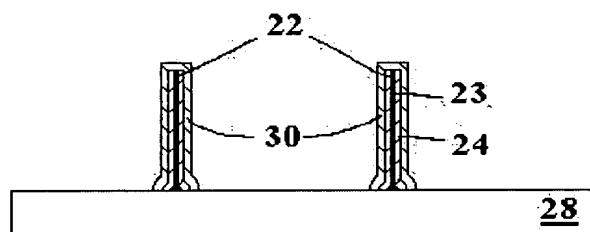


FIG. 4C

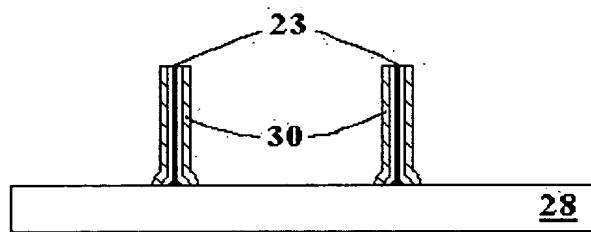


FIG. 4D

Title: Methods Of Making Vertical Electrical Feed Through Structures
Usable To Form Removable Substrate Tiles In A Wafer Test System
Docket No.: FACT-01001US1 Inventor: Mathieu, et al.
Serial No.: Unassigned " Attorney: Thomas A. Ward
Confirm No: Unassigned Phone: (415) 362-3800
Filed: Herewith Express Mail No.: EL 977910515 US
Sheet 4 of 9

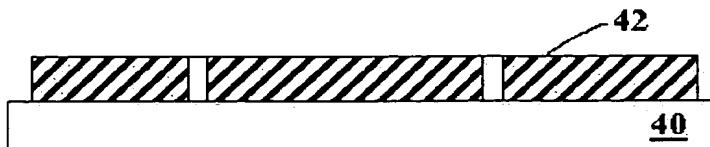


FIG. 5A

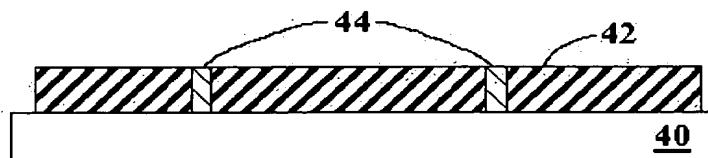


FIG. 5B

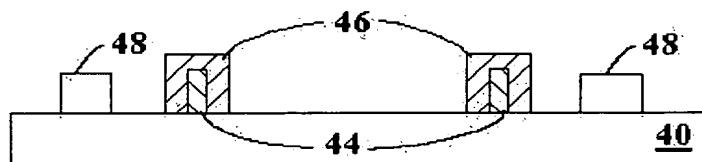


FIG. 5C

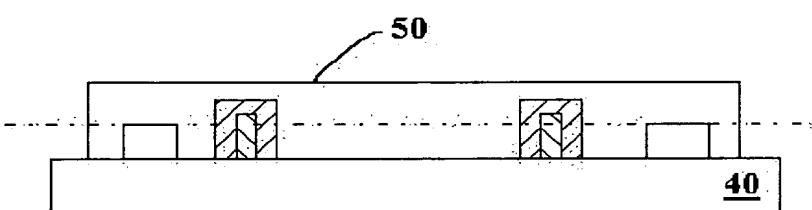


FIG. 5D

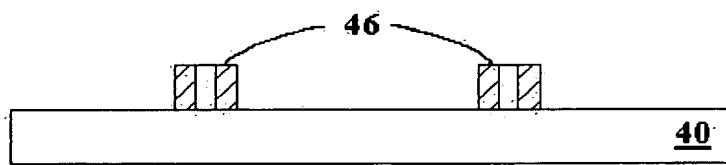


FIG. 5E

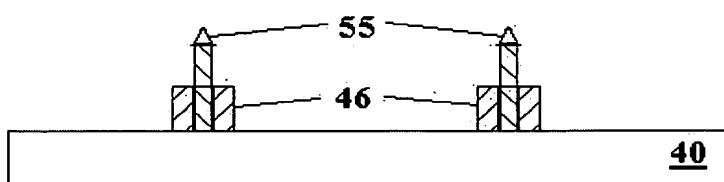


FIG. 6

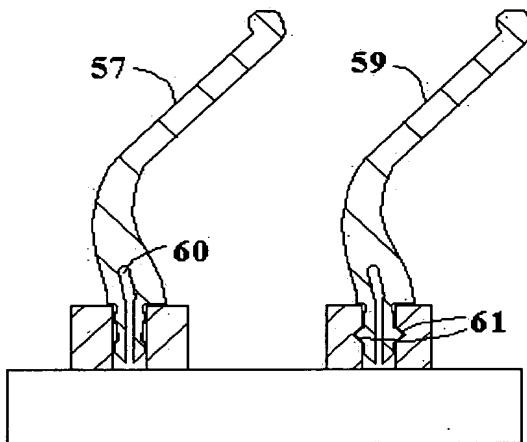


FIG. 7

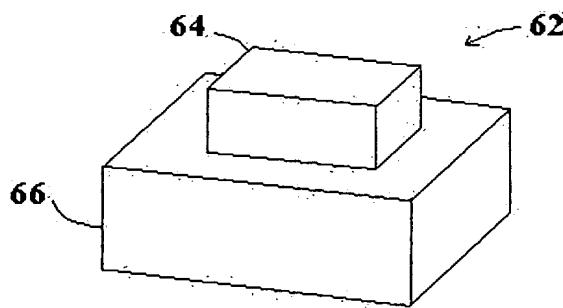


FIG. 8A

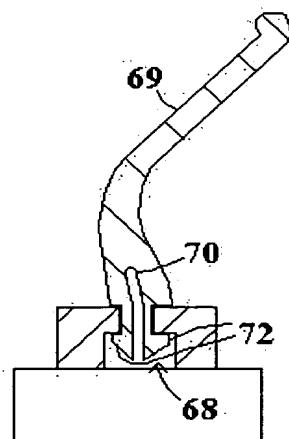


FIG. 8B



FIG. 9A

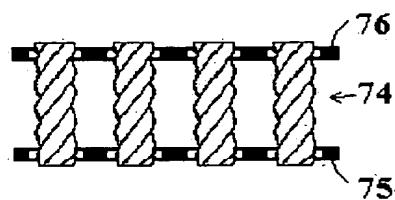


FIG. 9B



FIG. 9C

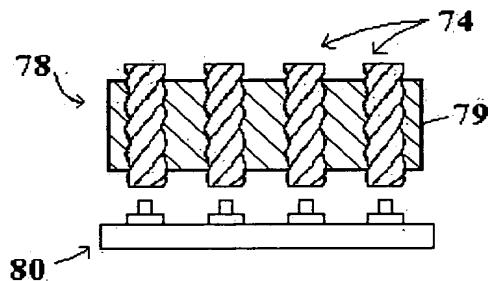


FIG. 9D

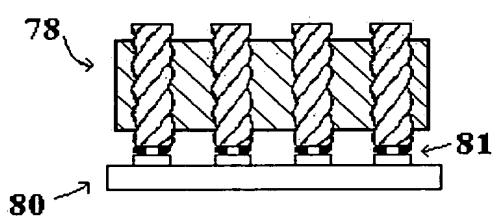


FIG. 9E

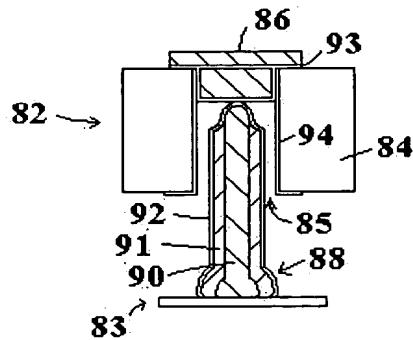


FIG. 10

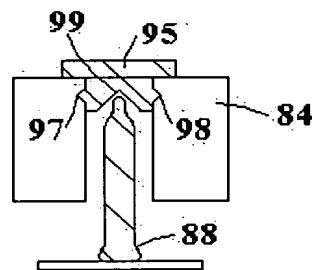


FIG. 11

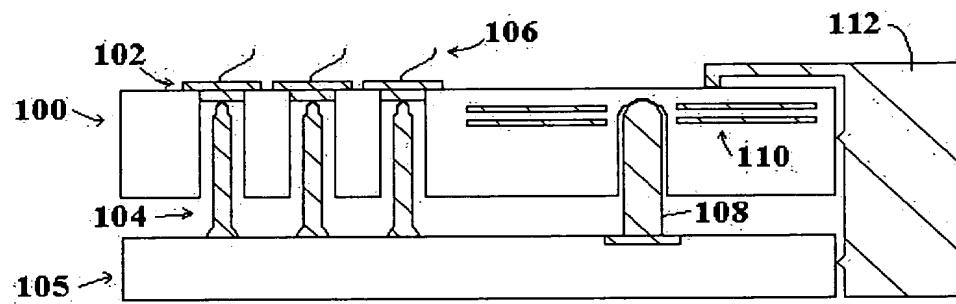


FIG. 12

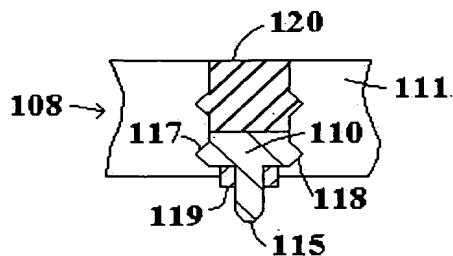


FIG. 13A

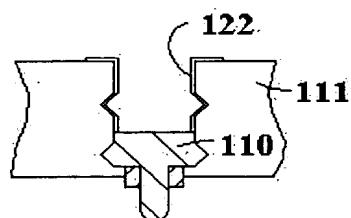


FIG. 13B

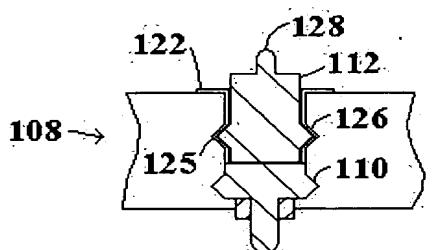


FIG. 13C

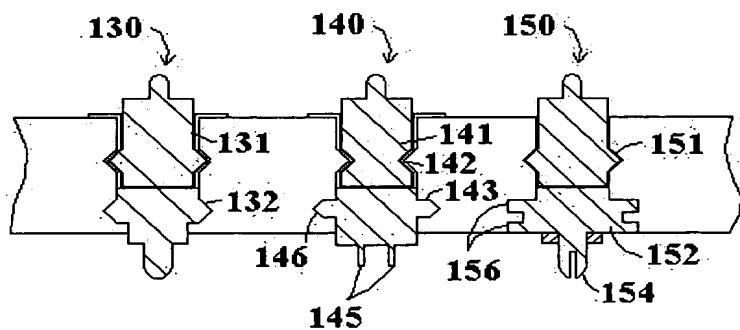


FIG. 14

Title: Methods Of Making Vertical Electrical Feed Through Structures
Usable To Form Removable Substrate Tiles In A Wafer Test System
Docket No.: FACT-01001US1 Inventor: Mathieu, et al.
Serial No.: Unassigned Attorney: Thomas A. Ward
Confirm No.: Unassigned Phone: (415) 362-3800
Filed: Herewith Express Mail No.: EL 977910515 US

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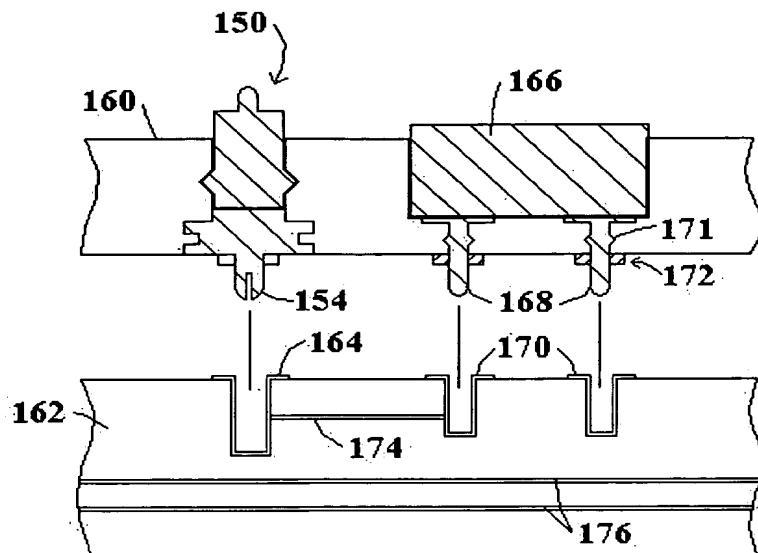


FIG. 15

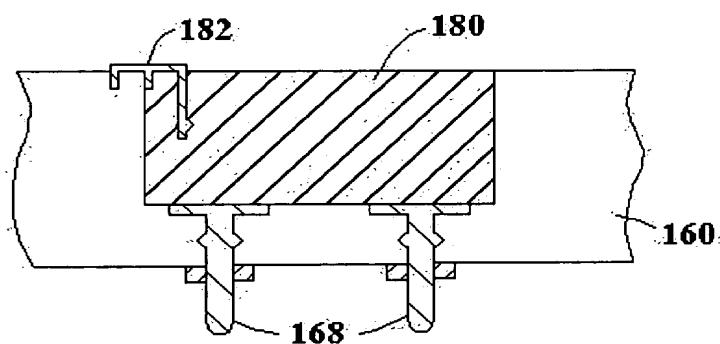


FIG. 16A

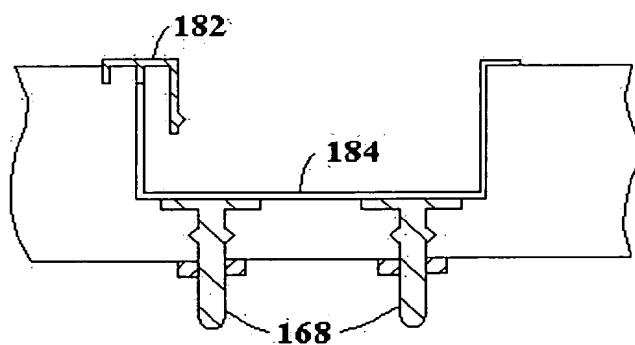


FIG. 16B